

Docket No.: 049657-0217



⁹²
B/IFW
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Shigeki OHBAYASHI	:	Confirmation Number: 8805
Application No.: 09/196,136	:	Group Art Unit: 2133
Filed: November 20, 1998	:	Allowed: June 16, 2005
	:	Examiner: Lamarre, Guy J.
For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING BONDING OPTIONAL FUNCTION	:	

REQUEST FOR ACKNOWLEDGEMENT OF THE CITED ART

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

We are in receipt of the Notice of Allowance dated June 16, 2005.

In reviewing the file, it is noted that we have not received acknowledgement of the Nikkei Electronics article submitted with the Information Disclosure Statement filed November 20, 1998. Applicants furnished a partial English translation (copy attached) of the relevant sections of this publication with the Supplemental Response filed August 20, 2004. Enclosed are copies of the Information Disclosure Statement, PTO-1449 and stamped return postcard acknowledgement.

Applicant hereby respectfully requests that:

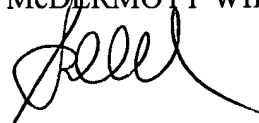
- (1) the appropriately acknowledged PTO-1449 be furnished to Applicant; and

No.: 09/196,136

(2) the record be clarified to confirm that the cited art has been considered and made of record.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

A handwritten signature in black ink, appearing to read 'S. Becker', written over the printed name.

Stephen A. Becker
Registration No. 26,527

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 SAB:cg
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Date: June 29, 2005

**Please recognize our Customer No. 20277
as our correspondence address.**



Applicant: Shigeki Ohbayashi Docket No. DA 49657-217
Title/Mark: Semiconductor Integrated Serial/Reg./Patent No. _____
Date Sent: 11/20/98 ☒ Hand Carried ☐ 1st-Class Mail ☐ Cert. of Mailing ☐ Express Mail No. _____
☐ Transmittal Letter
☒ Utility/Design/Continuation/CIP/FWC/Divisional/PCT
☐ Other: _____
 38 pages of Specification
 5 pages of Claims
 1 pages of Abstract
 13 pages of Formal/Informal Drawings
☒ Information Disclosure Statement
 1 Form PTO 1449
 7 copies of cited references
☐ Preliminary Amendment
☐ Response to Missing Parts Notice
☒ Certified Copy of Priority Doc.
☒ Claim for Convention Priority
☐ Verified Statement of Small Entity Status
☐ Transmittal of Issue Fee: Form PTO 85b _____ Form PTO 85C _____
☐ Maintenance Fee for _____ years after grant
☐ Notice of Appeal ☐ Appeal Brief
☒ Recordation of 1 Patent _____ Trademark: Assignments/Security Agreements/
☐ Response/Amendment to Office Action of _____ ☐ Petition to Commissioner for _____
☐ Trademark/Service Mark/Intent-To-Use Application
☐ Amendment to Allege Use ☐ Statement of Use
☐ Formal/Informal/Substitute Drawing Sheet
 _____ (no. of) Substitute/Specimens
☐ Continued Use Affidavit/Declaration (Section 8 & 15)
☐ Notice of Opposition/Answer to _____
☐ Ex Parte Appeal
☐ Petition for Cancellation/ Answer to _____
☐ Renewal of Reg. No. _____
☒ Combined Declaration/Power of Attorney
☐ Letter of Protest
☐ Request for _____ day/month Extension of Time
Other: _____



Check for \$ _____ ☒ Total Deposit Account charge for \$ 800 Attorney: SAR:4233 Atb
☐ CHI ☒ WASH

CLO2 Descrip.: 1-760 4-40

THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.

Early 2000 #

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shigeki OHBAYASHI

Serial No.:

Filed: November 20, 1998

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING BONDING OPTIONAL
FUNCTION



Group Art Unit:

Examiner:

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.


This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Stephen A. Becker
Registration No. 26,527

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Alexandria, Virginia 22314
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Date: November 20, 1998
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INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)			ATTY. DOCKET NO. 49657-217		SERIAL NO.	
			APPLICANT Shigeki OHBAYASHI			
			FILING DATE November 20, 1998		GROUP	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,703,510	Dec. 30, 1997	Iketani et al.			
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						Yes No
	06-069425	March 11, 1994	Japan (with English abstract)			
	06-005069	Jan. 14, 1994	Japan (with English abstract)			
	02-302987	Dec. 14, 1990	Japan (with English abstract)			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
	JEDEC Standard No. 21-C, JEDEC, December 22, 1994, pp. 3.7.8-7					
	"BGA Packages, Gaining Popularity in U.S.; Make High Density Mounting of High Pin Count LSIs Easy", A. Fukuda, Nikkei Electronics, Feb. 14, 1994, pp. 59-73.					
	M5M5V1132FP, GP-6, -7, -8, -7L, -8L, Mitsubishi's Product Data Sheet March 24, 1997, pp. 1-14.					
EXAMINER				DATE CONSIDERED		

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.